



Contribution ID: 104

Type: Talk

Forward silicon vertex/tracking detector design and R&D for the future Electron-Ion Collider

Wednesday 8 September 2021 16:00 (20 minutes)

The proposed high-luminosity high-energy Electron-Ion Collider (EIC) will provide a clean environment to precisely study several fundamental questions in the high energy and nuclear physics fields. To realize the proposed physics measurements at the EIC, a high granularity detector, which can cover pseudorapidity range from -3.5 to 3.5, provide percentage momentum/energy resolution and be able to separate nanosecond bunch crossings, is required. Within the EIC general detector, a low material budget silicon vertex/tracking detector with fine spatial resolution is critical to carry out a series of hadron/jet measurements at the EIC especially for the heavy flavor product reconstruction/tagging. We will present designs of a proposed forward silicon tracking detector (with pseudorapidity coverage from 1 to 3.5) integrated with different magnet options and the other detector sub-systems for the EIC. The evaluated performance of this detector meets the EIC detector requirements. The detector R&D work for the silicon technology candidates: Low Gain Avalanche Diode (LGAD) and radiation hard Monolithic Active Pixel Sensor (MALTA) will be shown as well.

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Session Classification: Development of accelerators and detectors

Track Classification: Development of accelerators and detectors